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Critical Parameter Design for a Cascaded H-Bridge With Selective Harmonic Elimination/Compensation Based on Harmonic Envelope Analysis for Single-Phase Systems

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Abstract—This paper investigates the critical parameter design technique for a cascaded H-Bridge (CHB) with selective harmonic elimination (SHE)/compensation (SHC) for single-phase systems. The critical parameters include the filter inductance, the number of H-bridge cells, the dc-bus voltage and the switching frequency. This paper derives the relationship of electrical parameters, topologies, and modulation techniques to the voltage/current harmonic envelopes, and compares the filter inductor design for an H-Bridge with sinusoidal pulsewidth modulation (SPWM), an H-Bridge (HB) with SHE/SHC and a CHB with SHE/SHC. Furthermore, a harmonic-envelope-based approach is developed to determine the optimum design parameters. The developed approach designs the harmonic envelope with two proposed criteria: the first one is the capacity criterion, which guarantees that the fundamental and controllable components are within the applicable solution range to avoid overmodulation; the other one is the attenuation criterion, which ensures that the uncontrollable harmonic complies with the grid limit. Simulations and experiments verified that a CHB designed with the proposed technique can simultaneously fulfill the compensation objectives and meet the harmonic limits.

Index Terms—Cascaded H-Bridge (CHB), compensation capacity, critical parameter design, filter design, harmonic analysis, harmonic attenuation requirement, selective harmonic elimination (SHE), compensation (SHC).

I. INTRODUCTION

CASCADED H-Bridge (CHB) with selective harmonic elimination (SHE)/compensation (SHC) is a promising candidate in modular high-power electronics applications, such as grid-tied photovoltaic (PV) inverters, wind farms, and electric vehicle charging stations[1]–[3]. A CHB with SHE can control fundamental component to absorb/inject active power [4]–[8], to

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TABLE I CRITICAL PARAMETERS IN A CHB CIRCUIT

Number of cells	DC voltage of each cell	Filter inductance	Number of switching transitions
N	Ε	L	K

compensate reactive power [5], [9] and to compensate the threephase unbalanced currents [10]. Furthermore, a CHB with SHC can also inject the selected high-order harmonics to compensate grid high-order harmonics [5], [11]–[13] as active power filters (APFs).

Although all the objectives in [4]–[13] can be fulfilled, the design of critical parameters, such as filter parameters, number of cells, switching frequencies, and dc-bus voltages is obscure. Existing techniques mainly focus on the parameter [14]–[17], circuit structure [15] and the modeling [18] of the coupling filters (for simplification, filters will be used to represent coupling filters in the later part of the paper) between CHBs and the power grid. Rockhill *et al.* [16] and Zabaleta *et al.* [17] investigate the filter design based on switching frequencies in low-switching frequency applications. However, the critical parameter design also depends on circuit topologies and modulation techniques. A new design procedure should be developed.

CHB circuits have more critical parameters to design than an H-Bridge (HB) circuit, as given in Table I, so it is necessary to investigate how each parameter influences the current/voltage harmonics.

The output voltage of H-Bridge has harmonics. In this paper controllable harmonics are defined as the harmonics whose magnitude and phase can be controlled with the switching angles of the staircase waveforms in SHE/SHC or with the modulation reference signal in sinusoidal pulsewidth modulation (SPWM). The fundamental is also controllable. Not all harmonics are controllable due to the limited number of switching angles in SHE/SHC or the limited switching frequency in SPWM. The harmonics out of control are defined as uncontrollable harmonics in this paper.

With SPWM, the closed-form expression for harmonics and the required filter insertion loss can be derived [16], [19]. The most critical harmonics which determine the filter design are

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close to the equivalent switching frequencies of the CHB and the expression for their magnitudes has been derived. So, the filter can be designed based on critical harmonics. However, SHE/SHC is a numerical algorithm and only focuses on the selected harmonics. All other harmonics are uncontrollable. The uncontrollable harmonics are widespread in the spectrum, and their magnitudes are unknown. As a result, the filter cannot be easily designed.

Most literature such as [17] design filters based on trial-anderror to meet the grid limit. But, this procedure is very timeconsuming and lacks the theoretical understanding of the filter, as a result, the size/volume of the inverters is not optimized.

In the SHE/SHC of [8], [10]–[13], the uncontrollable current harmonics cannot comply with the grid limit. For the SHE in [4] and [7], only selective voltage harmonics are eliminated, and it is uncertain if the currents resulted from the uncontrollable voltage harmonics can meet the grid limit. For the SHE in [6], the filters are overdesigned as all current harmonics are far below the grid limit. Current mitigation techniques [9] can comply with the grid limit, but the exhaustion method is used.

Another critical issue is that filter design mainly considers how to attenuate the uncontrollable harmonics [14]–[18]. However, critical parameter design depends on not only the uncontrollable harmonics, but also controllable the fundamental and harmonics. In this paper, the envelope of the fundamental and controllable harmonics is defined as compensation capacity of the CHB; and the filter's attenuation to uncontrollable harmonics is defined as attenuation ability. It is well known that a large filter not only increases attenuation ability, but also decreases the compensation capacity. Therefore, the design of the compensation capacity and the design of the attenuation ability are a dilemma. Other parameters such as switching frequency and the number of cells must be considered in filter design, otherwise it is difficult to meet both the compensation capacity and attenuation ability requirement.

This paper explores the relationship of critical parameters, topologies, and modulation techniques to the envelopes of voltage/current harmonics. Based on the harmonic envelope, the paper developed a simple and straightforward technique to design all critical circuit parameters for CHBs with SHE/SHC. With the designed parameters, CHBs with SHE/SHC can meet both compensation capacity and attenuation ability requirements.

The paper is organized as follows. Section II analyzes and compares the harmonics of three different techniques: one HB with SPWM, one HB with SHE/SHC, and a CHB with SHE/SHC. Section III proposes two criteria for parameter design: The criterion for compensation capacity and the criterion for attenuation ability. A design technique is developed for critical parameters to guarantee that the output currents can meet both criteria. In Section IV, simulations and experiments are conducted to verify that the parameters designed with the proposed technique can fulfill the compensation capacity, and the uncontrollable harmonics can meet the harmonic limits.

II. HARMONIC ANALYSIS AND COMPARISON

This section will analyze the voltage/current spectrum for different topologies with different modulations. The harmonic



Fig. 1. Topology and modulation principle of an HB with SPWM. (a) Circuit topology. (b) Modulation diagram.

envelope is critical in parameter design. Appropriate parameters shall guarantee that the compensation capacity is large enough (i.e., the envelope of the fundamental and controllable harmonics should be greater than the reference of the voltage/current of a CHB), and the attenuation ability is enough (i.e., the maximum uncontrollable harmonic is below the harmonic limits). Because the compensation capacity is determined by the fundamental component and controllable harmonics, while the harmonic attenuation requirement is determined by the magnitude difference of the uncontrollable harmonics and the harmonic limits, this section separates the whole frequency range into two sections: the controllable harmonic frequency range which includes the fundamental frequency component and controllable harmonics, and the uncontrollable harmonic frequency range. Therefore, the controllable frequency range, the envelope of the magnitude of the controllable harmonics and the envelope of the magnitude of the uncontrollable harmonics will be investigated.

A. HB With SPWM technique

The topology and modulation principle of an HB with SPWM are shown in Fig. 1. The harmonics of the output voltage can be derived with dual Fourier analysis [19]. The modulation with naturally sampled reference and double-edge carrier is given as an example here. The output voltage of CHB is mostly determined by the modulation component and the sideband harmonics of the carrier (switching) frequency as given by

$$v_{\rm HB-SPWM}(t) \approx \underbrace{MV_{\rm DC}\cos(\omega_m t)}_{\rm Modulation\ Component} + \frac{2}{\pi} V_{\rm DC} \sum_{n=-\infty}^{\infty} J_{2n-1}(\pi M) \cos(n\pi) \cos\left((2\omega_C + (2n-1)\omega_m)t\right)$$

$$= v_{M_HB-SPMW}(t) + v_{H_HB-SPWM}(t)$$
(1)

where *n* is the baseband index [16], which is related to the sideband harmonics, *M* is the modulation index, V_{DC} is the dcbus voltage of the HB, ω_m is the modulation frequency which could be equal to grid frequency ω_g or a grid harmonic to be controllable, $\omega_C = K\omega_g$ is the carrier (switching) frequency, and $J_{2n-1}(\pi M)$ is the Bessel function [19]. The sidebands of switching harmonics are ignored in (1) as their magnitudes are much smaller than that of fundamental switching frequency.

In (1), $v_{M_HB-SPWM}(t) = MV_{DC}\cos(\omega_m t)$ is the controllable modulation component, and the magnitude $|V_{M_HB-SPWM}|$ is $MV_{DC}.M$ should be smaller than one. Also, $v_{H_HB-SPWM}(t) = \frac{2}{\pi}V_{DC}\sum_{n=-\infty}^{\infty}J_{2n-1}(\pi M)\cos(n\pi)\cos((2\omega_C + (2n-1)\omega_m)t)$ is the uncontrollable sideband harmonics. Their frequencies equal to $2\omega_C + (2n-1)\omega_m$. The magnitude represented by $|V_{H,\text{HB-SPWM}}|$ equals to $2V_{\text{DC}}|J_{2n-1}(\pi M)|/\pi$. Because $|J_{2n-1}(\pi M)| \leq 0.5819$ and it reaches the maximum value when M = 0.5860 and n = 0 or 1, the frequencies of the highest harmonics are $\omega = (2\omega_C - \omega_m)$ and $(2\omega_C + \omega_m)$ [19]. The maximum magnitude is $|V_{H}|$ -HB – SPWM $|_{\text{max}} = 0.5819V_{\text{DC}} \times 2/\pi = 0.3704V_{\text{DC}}$.

Generally, when the controllable frequency range includes not only ω_g , but also the harmonics of ω_g , the envelope of the maximum voltage spectrum of all modulation conditions is given by

$$\begin{cases} |V_{M_HB-SPMW}|_{max} = V_{DC} & \omega \le \omega_{HB-SPWM-CPS} (2) \\ |V_{M_HB-SPMW}|_{max} = 0.2704 V & (2.5) \end{cases}$$

$$\left(\left|V_{H_\text{HB}-\text{SPWM}}\right|_{\text{max}} = 0.3704 V_{\text{DC}} \quad \omega = (2\omega_C \mp \omega_m) \quad (3)$$

where $\omega_{\text{HB-SPWM-CPS}}$ is the maximum controllable frequency range. If single inductor L_{FLT} is used as the filter, the current envelope can be expressed as (4), (5) and (6) as shown at the bottom of this page, where, $I_{F_\text{HB-SPWM}}$ is the fundamental current component, $I_{M_\text{HB-SPWM}}$ is the controllable current harmonic, $I_{H_\text{HB-SPWM}}$ is the uncontrollable current harmonic, and $h = \omega_m / \omega_g$ is the harmonic order of grid frequency.

Because $|J_{2n-1}(\pi M)/(2\omega_C + (2n-1)\omega_m)|$ reaches the maximum when M = 0.5860 and n = 0, the maximum current harmonic can be derived from (6)

$$\left|I_{H_\text{HB-SPWM}}\right|_{\text{max}} = 0.3704 V_{\text{DC}} / \left(2\omega_C - \omega_m\right) L_{\text{FLT}}.$$
 (7)

Choose the base value as $V_{\text{base}} = V_{\text{DC}}$, $Z_{\text{base}} = \omega_g L_{\text{FLT}}$, then the per-unit voltage and current envelopes are given by (8)–(12), respectively, as shown at the bottom of this page.

The controllable frequency range, $\omega_{HB-SPWM-CPS}$, is limited by several factors, including the limitation derived from the sampling theorem, the phase delay caused by modulation process and the energy band of the uncontrollable harmonics [22]. A common range employed in industrial applications is

$$\omega_{\rm HB-SPWM-CPS} = \omega_C / 5 = K \omega_g / 5. \tag{13}$$



Fig. 2. Per unit voltage and current spectrum envelopes of an HB with SPWM ($K = 15, \omega_m = \omega_g$).

As an example, the voltage and current envelopes are shown in Fig. 2 with $\omega_{\rm C} = 15\omega_g$. The solid lines represent the controllable envelopes for voltage (black) and current (blue), and they are calculated from (8) and (11). The dot lines represent the uncontrollable switching harmonics which should be attenuated. As shown in (1), the maximum voltage magnitude is $2V_{\rm DC}|J_{2n-1}(\pi M)|/\pi$ for the harmonic at frequency $2\omega_C + (2n-1)\omega_C$. The maximum value of Bessel function $|J_{2+-1}(\pi M)|$ with $M \le 1$ can be calculated using MATLAB. The envelope of the sideband harmonics looks asymmetrical because x-axis is in log-scale.

It is assumed in (1) that modulation component has only one frequency. For APFs which control multiple grid harmonics, [20] derived the closed-form expression for sideband harmonics, and proved that the highest magnitude of the uncontrollable harmonics is smaller than (1). The sideband harmonics in (1) are thus the worst scenario for filter design.

B. HB With SHE/SHC

The half-wave symmetry modulation is used as an example. The topology and waveform are shown in Fig. 3. The switching

$$\begin{cases} |I_{F_HB-SPMW}|_{max} = \frac{(V_{DC} - V_g)}{\omega_g L_{FLT}} & \omega_m = \omega_g \\ |I_{M_HB-SPMW}|_{max} = \frac{V_{DC}}{\omega_m L_{FLT}} = \frac{V_{DC}}{\hbar \omega_g L_{FLT}} & 3\omega_g \le \omega_m \le \omega_{HB-SPWM-CPS} \\ |I_{H_HB-SPWM}|_{max} = \frac{2V_{DC}}{\pi L_{FLT}} \left| \frac{J_{2n-1} (\pi M)}{2\omega_C + (2n-1)\omega_m} \right|_{max} & \omega \ge \omega_{HB-SPWM-CPS} \end{cases}$$
(6)

ſ	$ V_{M_\mathrm{HB-SPMW}} _{\mathrm{max}}^{p} = 1$	$\omega_m \leq \omega_{\mathrm{HB-SPWM-CPS}}$	(8)
J	$ V_{H_\text{HB-SPWM}} _{\text{max}}{}^p = 0.3704$	$\omega = (2\omega_C \mp \omega_m)$	(9)
ſ	$\left I_{F_{\rm -HB-SPMW}}\right _{\rm max}{}^{p} = \left(V_{\rm DC} - V_{g}\right)/V_{\rm DC}$	$\omega_m = \omega_g$	(10)
ł	$\left I_{M_\mathrm{HB-SPMW}}\right _{\mathrm{max}}^{p} = 1/h$	$3\omega_g \le \omega_m \le \omega_{\rm HB-SPWM-CPS}$	(11)
	$ I_{H,HB-SPWM} _{max}^{p} = 0.3704/h$	$\omega = 2\omega_C - \omega_m.$	(12)



Fig. 3. Topology and waveform of an HB with SHE/SHC modulation. (a) Circuit topology. (b) Output voltage waveform.

frequency is $K\omega_q$. The maximum harmonic is derived (A9)

$$\left| V_{\text{HB-SHE}/C} \right|_{\text{max}} = \begin{cases} 4V_{\text{DC}}/\pi, & h \le K \\ 4KV_{\text{DC}}/h\pi, & h \ge K \end{cases}$$
(14)

The envelope of the maximum output current with inductor L_{FLT} as a filter is, therefore

$$|I_{\rm HB-SHE/C}|_{\rm max} = \begin{cases} \left[(4V_{\rm DC}/\pi) - V_g \right] / (\omega_g L_{\rm FLT}), & h = 1 \\ 4V_{\rm DC} / (h\pi\omega_g L_{\rm FLT}), & 3 \le h < K \\ 4KV_{\rm DC} / (h^2\pi\omega_g L_{\rm FLT}), & h \ge K \end{cases}$$
(15)

Similar to Section II-A, the base values are selected as $V_{\text{base}} = V_{\text{DC}}, Z_{\text{base}} = \omega_g L_{\text{FLT}}$. The per-unit voltage/current envelopes are

$$\left|V_{\mathrm{HB-SHE}/C}\right|_{\mathrm{max}}^{p} = \begin{cases} 4/\pi, & h < K\\ 4K/(h\pi), & h \ge K \end{cases}$$
(16)

$$|I_{\rm HB-SHE/C}|_{\rm max}{}^{p} = \begin{cases} 4/\pi - V_g/V_{\rm DC}, & h = 1\\ 4/(h\pi), & 1 \le h < K \\ 4K/(h^2\pi), & h \ge K \end{cases}$$
(17)

For the controllable frequency range, the total number of the controllable harmonics of SHE/SHC equals to K. Because a filter has large attenuation in high-frequency range, to fully utilize its attenuation ability, most applications choose low-frequency harmonics as the controllable harmonics. The frequencies of the controllable harmonics are $\omega_g, 3\omega_g, \ldots, (2K-1)\omega_g$. Even order harmonics are zero because of half waveform symmetry. The range of the controllable harmonics is, therefore, $[0, \omega_{\text{HB-SHE}/C-\text{CPS}}]$, where,

$$\omega_{\rm HB-SHE/C-CPS} = 2K\omega_g. \tag{18}$$

The voltage and current spectrum envelopes are shown in Fig. 4 when K = 15 from (16) and (17).

C. CHB With SHE/SHC

The topology and waveform of a CHB with SHE/SHC is shown in Fig. 5. Similar to Section II-B, the half-wave symmetrical modulation is used as an example. The maximum harmonic



Fig. 4. Per unit voltage and current spectrum envelopes of an HB with SHE/SHC (K = 15).



Fig. 5. Topology and waveform of a CHB with SHE/SHC. (a) Circuit topology. (b) Output voltage of each cell. (c) Overall output voltage $v_{C\,\rm HB-SHE/C}$.

is derived in the Appendix as (A14)

$$\left|V_{\text{CHB-SHE}/C}\right|_{\text{max}} = \min\left(\frac{4KE}{h\pi}, \frac{4NE}{\pi}\right) = \begin{cases} 4NE/\pi, & h < K/N\\ 4KE/(h\pi), & h \ge K/N \end{cases}$$
(19)

$$|I_{\text{CHB-SHE}/C}|_{\text{max}} = \begin{cases} [(4NE/\pi) - V_g]/(\omega_g L_{\text{FLT}}), & h = 1\\ 4NE/(h\pi\omega_g L_{\text{FLT}}), & 3 \le h < K/N \\ 4KE/(h^2\pi\omega_g L_{\text{FLT}}), & h \ge K/N \end{cases}$$
(20)

Voltage (p.u.) 10.0

V_{HB-SHE/C} ma

VCHB-SHE/C



0.001 5 10 15 Harmonic Order, h 30 100 3 (a) IHB-SPWM max Current (p.u.) 0.01 IHB-SHE/C max I_{CHB-SHE/C} 0.0 Staircas 01 5 10 15 Harmonic Order, h 3 (b)

Fig. 6. Per unit voltage and current spectrum envelopes of a CHB with SHE/SHC. (a) N = 3, K = 15. (b) N = 15, K = 15 (staircase).

Choosing $V_{\text{base}} = NE$, $Z_{\text{base}} = \omega_g L_{\text{FLT}}$ as base values, the per-unit voltage and current envelopes are

$$\left|V_{\text{CHB-SHE}/C}\right|_{\max}^{p} = \begin{cases} 4/\pi, & h < K/N\\ 4K/(h\pi N), & h \ge K/N \end{cases}$$
(21)

$$\left| I_{\text{CHB-SHE}/C} \right|_{\text{max}}^{p} = \begin{cases} 4/\pi - V_g/(NE), & h = 1\\ 4/(h\pi), & 3 \le h < K/N \\ 4K/(h^2\pi N), & h \ge K/N \end{cases}$$
(22)

The controllable harmonic range is $[0, \omega_{CHB-SHE/C-CPS}]$, and is determined by the number of switching transitions K

$$\omega_{\rm CHB-SHE/C-CPS} = 2K\omega_g. \tag{23}$$

Fig. 6 shows the envelopes of the voltage and current spectra of a CHB with SHE/SHC. They are derived from (21) and (22). The corner frequency is $K\omega_g/N$ as shown by the harmonic order in (21). When K = N, each HB switches only once within one period. The modulation is known as staircase modulation and the converter would reach the minimal switching loss.

D. Harmonic Comparison and the Features of an HB With SPWM, SHE/SHC and a CHB With SHE/SHC

This section will compare the harmonics from different topologies and different modulation techniques. Three features of the harmonics are discussed: the controllable frequency range, the maximum magnitude of controllable harmonics (including the fundamental harmonics) and the maximum magnitude of the uncontrollable harmonics.

The voltage/current envelopes comparison is shown in Fig. 7 with K = 15 and $NE = V_{DC}$. The solid curves represent the

Fig. 7. Comparison of the per unit harmonic envelopes when K=15 and $NE=V_{\rm DC}.$ (a) Voltage. (b) Current.

controllable envelopes, and the dot lines represent the harmonics to be attenuated. Blue, black, red, and pink curves represent different modulation techniques, as shown in Fig. 7.

If the three techniques in Sections III-A–III-C have the same number of switching transitions K, the controllable frequency range of different techniques can be calculated from (13), (18), and (23). The comparison is shown as

$$\omega_{\text{CHB-SHE}/C-\text{CPS}} = \omega_{\text{CHB-SHE}/C-\text{CPS}}$$
$$= 10\omega_{\text{HB-SPWM-CPS}} = 2K\omega_g. \quad (24)$$

Equation (24) indicates that the SHE/SHC can extend the controllable frequency range to ten times of the SPWM.

For the magnitude of fundamental component, from (8), (16) and (21), an HB or a CHB with SHE/SHC can achieve 127.39% fundamental voltage of an HB with the SPWM technique.

For the voltage magnitude of controllable harmonics, the CHB with SHE/SHC starts to decrease at -20 dB/dec after corner frequency $K\omega_g/N$ as shown in (21). In Fig. 7(a), the blue, pink, and red sold curves show that depending on K/N, a CHB with SHE/SHC can either increase controllable envelope (the red one) or decrease controllable envelope (the pink one) by changing the corner frequency $K\omega_g/N$. This indicates that the voltage magnitudes of the controllable harmonics in a CHB with SHE/SHC can be controllable harmonics.

For the current magnitudes of the uncontrollable harmonics, Fig. 7(b) shows that a CHB with SHE/SHC has much lower current harmonics than that of an HB with SPWM at the first significant frequency. The first significant frequency here is defined as the frequency at which the filter is designed based on its harmonic attenuation requirement. In Fig. 7(b), for the HB with SPWM, the first significant frequency is at 29th harmonic frequency and for the CHB with SHE/SHC, it is the first significant frequency is at 31st harmonic frequency. The pink (N = 15) and

	Controll	Maximum harmonic magnitude		
	-able f	Fundamental	Controllable	Uncontrollable
	range	component	harmonics	harmonics
An HB with	Small	Low	Madium	Madium
SPWM	$K\omega_g/5$	1 p.u.	Medium	Medium
An HB with	Large	High	Uich	High
SHE/SHC	$2K\omega_g$	4/π p.u.	nigii nigii	riigii
A CHB with	Large	High	Adjustable	Adjustable
SHE/SHC	$2K\omega_g$	4/π p.u.	Aujustable	Aujustable
A CHB with	Large	High	Lowest	Lowest
Staircase	$2K\omega_{o}$	$4/\pi$ p.u.	Lowest Lowest	

TABLE II VOLTAGE/CURRENT COMPARISON

TABLE III PARAMETERS OF THE NONLINEAR LOAD

R_L / Ω	$C_L / \mu F$	L_{LI} / mH	L_{L2} / mH
8	100	0.1	5

red (N = 5) curves (CHBs with SHE/SHC) have 19.12 dB or 5.12 dB lower uncontrollable harmonics at the first significant frequency than that of the blue curve (HB with SPWM).

The detailed comparison between three topologies and modulation methods is made in Table II. From Table II, a CHB with SHE/SHC has adjustable controllable voltage magnitude, the highest controllable frequency range and the lowest uncontrollable harmonics of the three. It, therefore, has great potential for four-quadrant converter, inverter, APF, and STATCOM applications.

III. PARAMETER DESIGN FOR A CHB WITH SHE/SHC

This section will discuss the critical parameter design for CHBs with SHE/SHC. The parameters include N, E, K, and $L_{\rm FLT}$. As shown in Fig. 5, the maximum output voltage is NE, which should be smaller than the maximum allowable voltage $V_{\rm max}$ which the grid can withstand. $4NE/\pi$ is the maximum fundamental voltage and shall be larger than the rated output peak voltage V_{rated} . So, NE is limited by a range as

$$\pi V_{\rm rated} / 4 < NE < V_{\rm max}.$$
 (25)

Because N is an integer, and E is determined by battery voltage, NE only has a few options within the range of (25). The procedure of designing NE is similar to the design of V_{DC} in an HB. For a fixed NE, the selection of N and E depends on voltage/power ratings, hardware cost, software complexity, etc. If low uncontrollable harmonics is preferred, small E and large N are always preferred as shown in (19).

The general voltage control principle for a CHB with SHE/SHC is shown below based on divided frequency ranges [11], where ω is harmonic frequency

($\omega \leq \omega_{ m CPS}$	$v_{\text{CHB-SHE}/C}$ is controlled by
J		desired values
J	$\omega_{\rm CPS} < \omega \le \omega_{\rm CTRL}$	The voltage references are set as zero
l	$\omega > \omega_{\rm CTRL}$	The harmonics are uncontrollable

where ω_{CPS} is desired maximum compensation frequency, and ω_{CTRL} is maximum controllable frequency.

Equations (26)-(29) shall be satisfied to meet both the compensation capacity criterion and harmonic limitation:

$$\begin{cases} h = 1, \qquad \left| I_{\text{CHB-SHE}/C} \right|_{\text{max}} > I_{\text{FUN}} \quad (26) \\ 1 < h \le H_{\text{CPS}}, \qquad \left| I_{\text{CHB-SHE}/C} \right|_{\text{max}} > I_{\text{HAR}} \quad (27) \\ H_{\text{CPS}} < h \le 2K, \quad \text{no requirement} \quad (28) \\ h \ge H_{\text{CPS}}, \qquad \left| I_{\text{CHB-SHE}/C} \right|_{\text{max}} < I_{\text{STD}} \quad (29) \end{cases}$$

$$|I_{\text{CHB}-\text{SHE}/C}|_{\text{max}} < I_{\text{STD}}$$
 (29)

where I_{FUN} and I_{HAR} are the compensation capacity requirements for fundamental and harmonics, respectively, and ISTD is the harmonic limitation; where $H_{\text{CPS}} = \omega_{\text{CPS}} / \omega_g$, is the desired maximum compensation harmonic order.

Substituting the first equation of (20) into (26), the maximum inductance $L_{\text{FUN},\text{MAX}}$ for fundamental can be derived

$$L_{\rm FUN,MAX} = \left(4NE/\pi - V_g\right)/\left(\omega_g I_{\rm FUN}\right)$$
(30)

where V_g is peak value of grid voltage. As shown in Fig. 7(b), the compensation capacity decreases from $(K/N)\omega_q$. To achieve the maximum compensation capacity within $[0, H_{CPS}\omega_a]$ and minimize uncontrollable harmonics, K should be chosen as

$$K = NH_{\rm CPS}.\tag{31}$$

Substituting (31) into (23), the controllable range is $[0, 2NH_{\text{CPS}}\omega_q]$, which is wider than the required compensation frequency range $[0, H_{CPS}\omega_q]$, which satisfies the frequency condition of the general voltage control principle above.

As in (20), increasing L_{FLT} decreases uncontrollable harmonic and is good to meet the harmonic limitation, but also decreases the compensation capacity. Therefore, harmonic compensation capacity can be used to determine the upper limits $L_{\text{CPS}_{\text{MAX}}}$ of L_{FLT} ; harmonic limit can be used to determine the lower limits $L_{\text{HAR}-\text{MIN}}$ of the L_{FLT} . In conclusion, any $L_{\text{FLT}} \in [L_{\text{HAR}_{\text{MIN}}}, \min(L_{\text{FUN}_{\text{MAX}}}, L_{\text{CPS}_{\text{MAX}}})]$ can guarantee that the output current satisfies (26)-(29). The design procedure is shown in Fig. 8.

When $L_{\text{HAR}_{\text{MIN}}} > \min(L_{\text{FUN}_{\text{MAX}}}, L_{\text{CPS}_{\text{MAX}}}), L_{\text{FLT}}$ has no solutions. In this situation, changing $L_{\rm FLT}$ can never satisfy the compensation capacity and harmonic limitation simultaneously, and redesigning other parameters is inevitable. From (22), increasing N while keeping NE and K/N constant equivalently increases K. This extends controllable frequency range and keeps the current envelope unchanged. Therefore, $L_{\text{FUN}_{MAX}}$ and $L_{\text{CPS}_{MAX}}$ are kept unchanged, and $L_{\text{HAR}_{MIN}}$ is reduced because the controllable frequency range is extended to higher frequencies. In conclusion, increasing N while keeping NE and K/Nconstant to satisfy $L_{\text{HAR}_{\text{MIN}}} < \min(L_{\text{FUN}_{\text{MAX}}}, L_{\text{CPS}_{\text{MAX}}})$ is a possible solution. From (22), increasing K only can either increase L_{HAR_MIN} due to increased uncontrollable harmonics magnitude, or decrease $L_{\text{HAR}-\text{MIN}}$ due to the extended controllable frequency range. It does not change the compensation capacity (L_{CPS_MAX} unchanged). So, the relationship between K and $L_{\text{HAR}_{\text{MIN}}}$ is complicated as shown in Fig. 9. Therefore, only increasing K is not recommended.

The detailed procedure to determine the critical parameters is shown in Fig. 8. Compared with the conventional techniques,



Fig. 8. Flow chart for parameter design. (a) Design flow chart. (b) Illustration of L_{CPS_MAX} and L_{HAR_MIN} .



Fig. 9. Minimal inductance versus *K* to comply with IEEE 519.

the proposed guideline uses polylines to determine how each parameter influences the output current. Moreover, using the current envelope to decide the parameter boundary is quick and straight-forward. The proposed guideline can easily identify the issue if compensation capacity and harmonic limits cannot be met at the same time. Iterations in Fig. 8(a) are only used to find the optimized parameters when this issue happens.

 $L_{\text{HAR},\text{MIN}s}$ with different topologies and modulation techniques as a function of K calculated from Fig. 8 are compared in Fig. 9 for minimal filter size comparison. The design parameters are: $V_{\text{DC}} = NE = 240 \text{ V}, I_L = 20 \text{ A} (14.15 \text{ A RMS value})$, the base value is $V_{\text{base}} = 240 \text{ V}, I_{\text{base}} = 20 \text{ A}; Z_{\text{base}} V_{\text{base}} = 12 \Omega$. The harmonic



Fig. 10. Topologies and control blocks for simulations and experiments. (a) CHB with SHE when works as an inverter. (b) Control diagram for a CHB with SHE. (c) CHB with SHC when works as an APF. (d) Control diagram for a CHB with SHC.

limits are from IEEE 519. The curve of N Cell CHB starts at K = N because each cell should switch at least once per fundamental period. Similarly, the curve of an HB with SPWM starts at K = 5 because the controllable range starts at $K\omega_g/5$, which should be larger than ω_g , as in (13). Fig. 9 shows that increasing N while keeping K unchanged can greatly reduce $L_{\text{HAR},\text{MIN}}$. When K is small, the relationship between K and $L_{\text{HAR},\text{MIN}}$ is not monotonous. $L_{\text{HAR},\text{MIN}}$ of CHBs with SHE/SHC with large N is much smaller than those of conventional HBs with SPWM and single HBs with SHE/SHC.

IV. SIMULATION AND EXPERIMENT VERIFICATION

Simulations and experiments were conducted on an 110 V/60 Hz, 14 A system to validate the developed design guideline for both CHB SHE and SHC applications. The circuit topology is shown in Fig. 10. Fig. 10(a) and (b) shows a grid-tied CHB working as an inverter with SHE and its control block. Fig. 10(c) shows a CHB working as an APF to compensate the harmonics generated by nonlinear load on the grid and to correct the grid power factor at the same time. Its control block is in Fig. 10(d). Both the control blocks in Fig. 10(b) and (d) are essentially the current mode control with an open-loop control structure. Because the harmonic envelopes in Fig. 6 and the design procedure in Fig. 8 are independent from the control structures, the critical parameter design does not depend on the control structures. It can be applied to voltage mode control, too. Simulations were conducted in MATLAB Simulink. IEEE 519 harmonic limitation was used.

For the grid-tied inverter in Fig. 10(a), the maximum allowable voltage V_{max} , is limited by the both voltage rating of devices and the standard requirements on the grid. For example, NEC article 690.7(C) on solar PV systems requires that for one- and two-family dwellings, the maximum PV system voltage is limited to 600 V [21]. For a four-quadrant inverter,



Fig. 11. Envelope of current spectrum to identify $L_{\text{HAR}_{-MIN}}$.

the rated voltage V_{rated} should be larger than the sum of grid voltage V_g and the voltage drop V_L across the inductor. It is assumed that the $N \leq 5$ and $K \leq 30$. As shown in Fig. 9, the minimal impedance of inductor $L_{\text{HAR}-\text{MIN}}^p$ is 0.48 p.u. Therefore, $V_L = 0.48 NE$, and $V_{\text{rated}} \ge (V_g + 0.48 NE)$. From (25), $\pi (V_g + 0.48 NE)/4 < NE < 600$, hence, 202 < NE < 600. In the experiment, the voltage of single battery is 12 V, so the number of batteries is between 16.8 and 50. In this paper, the smallest possible NE = 240 V with 20 batteries was used to reduce the magnitude of uncontrollable harmonics based on (20) and to reduce the system cost. N = 5 and E = 48 V i.e., four batteries in series for each cell. Based on (30), $L_{\text{FUN-MAX}} = 20.04 \text{ mH} (0.63 \text{ p.u.})$. Because only fundamental component need to be controllable for the inverter in Fig. 10(a), $H_{\text{CPS}} = 1$. From (31), K = 5. Based on the dashed current envelop tangent and below the IEEE 519 in Fig. 11, $L_{\text{HAR},\text{MIN}}$ can be identified as 16.75 mH (0.53 p.u.). The $L_{\text{HAR},\text{MIN}}$ can also be derived from Fig. 9. So, the range of L_{FTL} is [16.75 mH, 20.04 mH]. The per unit range of $\omega_q L_{\text{FTL}}$ is [0.53, 0.63] with $V_{\text{base}} = 240 \text{ V}, I_{\text{base}} = 20 \text{ A}.$ Therefore, a 20 mH inductor can meet the compensation capacity and harmonic requirement at the same time.

The simulated waveforms are shown in Fig. 12. The fundamental peak current is $14 \sqrt{2} \approx 20$ A, total harmonic distortion (THD) is 1.16%, all the harmonics are below the limit of IEEE 519 and PF equals to 1.0.

Although a five-cell CHB is applied, the CHB voltage in Fig. 12(a) is 9-level instead of the conventional 11-level. This is because asymmetrical four-quadrant modulation technique [5] was applied and in this modulation technique, the voltage of each cell can be either step-up or step-down within the first quadrant, so the output voltages of two cells can be canceled due to its cascaded operation. However, this asymmetrical modulation technique can greatly extend the modulation index range [5].

In Fig. 12(a), even SHE is applied, low-frequency harmonic I_g still exist. This is due to the distortion of V_g . Actual V_g is not ideally sinusoidal. To make sure that simulations can predict the actual grid currents, the V_g used in the simulations in Fig. 12(a) is from the measured data in the experiments. In Fig. 12(a), the third, fifth, and seventh components of V_g are 5.1 V, 2.4 V, and 1.4 V, respectively. The calculated corresponding harmonic currents are 0.15 A (0.7%), 0.04 A (0.2%), and 0.02 A (0.1%), respectively. They match the simulation results in Fig. 12 well.



Fig. 12. Simulated results for a CHB with SHE (M = 0.9, N = 5, K = 5). (a) Waveforms. (b) Spectrum of grid current I_g .

Fig. 13 shows the simulated results for a conventional HB using SPWM. The parameters are similar to the CHB with SHE/C case: $V_{DC} = 240$ V, switching frequency is $5f_g$, M = 0.90 and $L_{FLT} = 20$ mH. None of the THD and harmonics meet the grid limit. Moreover, Fig. 9 shows that an HB with SPWM requires $L_{HAR,MIN} = 35.7$ mH, which is larger than $L_{FUN,MAX} = 20.04$ mH. As shown in Fig. 13, the current cannot comply with the grid limit and is not applicable for the application.

The comparison between Figs. 12 and 13 validates that with the given parameters ($NE = V_{DC} = 240 \text{ V}$, $K_{CHB-SHE} = K_{HB-SPWM} = 5$, $L_{CHB} = L_{HB} = 20 \text{ mH}$), the CHB with SHE has smaller harmonics than those generated by the HB with SPWM. Fig. 9 also illustrates that the HB with SPWM requires to increase the switching frequency to at least $74f_g$ (4440 Hz) with the same inductance 20 mH to comply with the grid limit. Therefore, for applications requiring low-switching frequency and high-power density, multilevel converters with SHE is a promising technique. This conclusion is obvious. Figs. 12 and 13 verified Fig. 9.

As shown in Fig. 13, the major harmonics locate around $10\omega_g$ due to the sideband harmonics of $v_{\text{HB}-\text{SPWM}}$. From (1), the 7th, 9th, 11th, 13th harmonic of $V_{C\text{HB}}$ can be calculated as 42 V, 61 V, 61 V, and 42 V, respectively. Therefore, the



Fig. 13. Simulated results for an HB with SPWM (with $M = 0.9, f_{sw} = 5f_q$). (a) Waveforms. (b) Spectrum of grid current I_q .



Fig. 14. Experimental results for a CHB with SHE (M = 0.9, N = 5, K = 5). (a) Waveforms. (b) Spectrum of grid current I_g .

corresponding currents are 0.8 A (4%), 0.9 A (4.5%), 0.7 A (3.5%), and 0.4 A (2%). The simulation matches the calculation with only 0.5% difference. Other harmonics above 13th of I_g are relatively small, and they are caused by both the grid voltage distortions and the sideband harmonics of V_{CHB} .

Fig. 14 shows the experimental results corresponding to Fig. 12. The simulated and experimental results match well, and the differences between the simulated and measured of each order of harmonics are within 0.8%. These differences can be caused by the inaccuracy of switching angles, the battery voltages' fluctuation, the nonlinearity of the inductor, and the harmonics of V_g . Zhao and Wang [20] and Moeini *et al.* [23]



Fig. 15. Current spectrum envelopes with different parameters to identify $L_{CPS,MAX}$ and $L_{HAR,MIN}$. (a). N = 5, K = 15. (b) N = 5, K = 5. (c) N = 5, K = 30. (d) N = 3, K = 15.

derives the analytical solution to explain how the variation of each parameter would affect the output current. Because the current harmonics generated by the nonideality above are low, and they can be compensated actively [11], [12], the effects of nonideality on the parameter design are not discussed in the paper.

Experimental results corresponding to the HB with SPWM are not given because this paper mainly focuses on the critical parameter design for the CHB with SHE/SHC.

Fig. 10(b) is an APF application to compensate the thirdand fifth-order harmonics. The compensation capacity is 30% of the rated system current, $I_{\text{FUN}} = I_{\text{CPS}} = 0.3 I_{\text{rate}} =$ 6 A (RMS value 4.23 A). The critical parameters were designed based on the flowchart in Fig. 8: NE = 240 V based on (25);



Fig. 16. Hardware prototype of a CHB when works as an APF.



Fig. 17. Simulation results for the single-phase APF (N = 5, K = 15). (a) Waveforms. (b) Spectrum of nonlinear load current $I_{\rm NL}$. (c) Spectrum of grid current I_g .

the maximum N is 5, E = 48 V; because $H_{\text{CPS}} = 3$, K = 15. $L_{\text{FUN}_MAX} = 66.3 \text{ mH} (2.08 \text{ p.u.})$ based on (30); $L_{\text{CPS}_MAX} = 44.6 \text{ mH} (1.39 \text{ p.u.})$ and $L_{\text{HAR}-\text{MIN}} = 32.6 \text{ mH} (1.02 \text{ p.u.})$ can be derived based on Fig. 15(a). The range of the inductor is $L_{\text{FTL}} \in [32.6 \text{ mH}, 44.6 \text{ mH}]$, $\omega_g L_{\text{FTL}} \in [1.02, 1.39]$ p.u. with $V_{\text{base}} = 240 \text{ V}$, $I_{\text{base}} = 20 \text{ A}$. 35 mH is selected. Fig. 16 shows the hardware prototype corresponding to Fig. 10(c). Fig. 17 shows the simulation results. Fig. 15(b)–(d) illustrate that if parameters are not designed with the proposed guideline, adjusting inductance L_{FLT} may never meet the compensation capacity and harmonic limits simultaneously. In Fig. 15(b), N is unchanged while K is decreased; in Fig. 15(c), N is unchanged while K



Fig. 18. Experimental results for the single-phase APF (N = 5, K = 15). (a) Waveforms. (b) Spectrum of nonlinear load current $I_{\rm NL}$. (c) Spectrum of grid current I_g .

is doubled; in Fig. 15(d), K is the unchanged while N is decreased. In these cases, L_{CPS_MAX} is smaller than $L_{\text{HAR}_\text{MIN}}$, so adjusting L_{FLT} can never meet the compensation capacity and harmonic limits simultaneously. The effect of increasing N is not discussed in this paper because it is assumed that N has a limit.

With the APF design, the power factor is improved from 0.94 (lagging) to 0.99 (lagging); the third harmonic is reduced from 13.2% to 0.5%; and the THD is reduced from 14.57% to 3.86%. Furthermore, after compensation, both the controllable harmonics and the uncontrollable harmonics are below the grid limit, which proves that the proposed design technique can meet both the compensation and attenuation requirements. In Fig. 17, the third and fifth harmonics still exist because of the distortion on V_q . Uncontrollable harmonics still exist.

The proposed technique is further validated in experiments. A five-cell four-quadrant CHB converter is developed in Fig. 16. A TMS320F28335 DSP with a CPLD EP2C5T144C8 are used as the controller in the prototype. The experimental waveforms were recorded with an Rigol MSO 4504 digital oscilloscope. Fig. 18 matches Fig. 17, which further validates that the APF designed with the proposed technique is effective and can meet both the compensation and attenuation requirements.

V. CONCLUSION

A critical parameter design technique for a CHB with SHE/SHC was developed in this paper. The contributions of this paper are as follows.

- The influence of the critical parameters, topologies and modulation techniques to the harmonic envelope was analyzed. The controllable frequency range and the magnitudes of the fundamental, controllable harmonics, and uncontrollable harmonics are explored.
- 2) The minimal filter inductances for an H-Bridge with SPWM, an H-Bridge (HB) with SHE/SHC and a CHB with SHE/SHC have been compared.
- 3) Both the compensation and attenuation requirements are incorporated in the proposed design.

- 4) The design procedure is based on the harmonic envelope, which is simple and intuitive and can be used to evaluate the minimal filter.
- 5) The design technique can be applied to both inverters/converters and APFs, and has been verified by both simulations and experiments.

APPENDIX MAXIMUM VOLTAGE OF A HB AND A CHB CIRCUIT WITH SHE/SHC

A. HB With SHE/SHC

The topology and waveform are shown in Fig. 3, and the equation of the time domain waveform is given by

$$v_{\text{HB-SHE}/C}(t) = \begin{cases} V_{\text{DC}}, & \theta_1 < \omega t < \theta_2, \dots, \theta_{2K-1} < \omega t < \theta_{2K}, \dots \\ 0, & 0 < \omega t < \theta_1, \dots, \theta_2 < \omega t < \pi + \theta_{2K-1}, \dots \\ -V_{\text{DC}}, & \pi + \theta_1 < \omega t < \pi + \theta_2, \dots \end{cases}$$
(A1)

The voltage spectrum envelope can be derived with Fourier series as shown

$$\begin{aligned} v_{\mathrm{HB-SHE}/C}\left(t\right) &= \sum_{h=1}^{\infty} \left(a_{\mathrm{HB-SHE}/C-h}\cos\left(h\omega_{g}t\right) \\ &+ b_{\mathrm{HB-SHE}/C-h}\sin\left(h\omega_{g}t\right)\right) \\ &= \sum_{h=1}^{\infty} c_{\mathrm{HB-SHE}/C-h}\cos\left(h\omega_{g}t + \theta_{h}\right) \quad (A2) \\ &\left\{a_{\mathrm{HB-SHE}/C-h} &= 2/T_{g} \times \int_{0}^{T_{g}} v_{\mathrm{HB-SHE}/C}\left(t\right)\cos\left(h\omega_{g}t\right)dt \\ &b_{\mathrm{HB-SHE}/C-h} &= 2/T_{g} \times \int_{0}^{T_{g}} v_{\mathrm{HB-SHE}/C}\left(t\right)\sin\left(h\omega_{g}t\right)dt \end{aligned} \right.$$

where
$$a_{\text{HB}-\text{SHE}-h}$$
 and $b_{\text{HB}-\text{SHE}-h}$ are the magnitudes of cosine
and sine components of the *h*th order voltage harmonic, respec-
tively; $c_{\text{HB}-\text{SHE}/C-h}$ and θ_h are the magnitude and phase of the
*h*th order voltage harmonic, respectively.

The value of $c_{\text{HB-SHE}/C-h}$ can be calculated by

$$c_{\mathrm{HB-SHE}/C-h} = 2/T_g \times \int_0^{T_g} v_{\mathrm{HB-SHE}/C} (t) \cos \left(h\omega_g t + \theta_h\right) dt$$
(A4)

The envelope of the maximum voltage is given by

$$\begin{aligned} |c_{\mathrm{HB-SHE}/C-h}| &\leq 2/T_g \\ &\times \int_0^{T_g} |v_{\mathrm{HB-SHE}/C}(t)| \left|\cos\left(h\omega_g t + \theta_h\right)\right| dt \\ &\leq \frac{2}{T_g} V_{\mathrm{DC}} \int_0^{T_g} \left|\cos\left(h\omega_g t + \theta_h\right)\right| dt = \frac{4V_{\mathrm{DC}}}{\pi}. \end{aligned}$$
(A5)

On the other hand, substituting (A1) to (A3), $a_{\text{HB-SHE}/C-h}$ and $b_{\text{HB-SHE}/C-h}$ of Fourier series can be calculated as

$$\begin{cases} a_{\text{HB-SHE}/C-h} = -\frac{2V_{\text{DC}}}{\pi h} \left(\sum_{i=1}^{K} \left(\sin \left(h\theta_{ir} \right) - \sin \left(h\theta_{if} \right) \right) \right) \\ b_{\text{HB-SHE}/C-h} = \frac{2V_{\text{DC}}}{\pi h} \left(\sum_{i=1}^{K} \left(\cos \left(h\theta_{ir} \right) - \cos \left(h\theta_{if} \right) \right) \right) \end{cases}$$
(A6)

where θ_{ir} is the angle of the *i*th rising edge and θ_{if} is the angle of the *i*th falling edge.

If $V_{\text{HB-SHE}/C-h}$ is defined as a complex voltage as

$$V_{\text{HB-SHE}/C-h} = b_{\text{HB-SHE}/C-h} + ja_{\text{HB-SHE}/C-h}$$
$$= \frac{2V_{\text{DC}}}{\pi h} \left(\sum_{i=1}^{K} \left(\cos\left(h\theta_{ir}\right) - j\sin\left(h\theta_{ir}\right) \right) \\ -\sum_{i=1}^{K} \left(\cos\left(h\theta_{if}\right) - j\sin\left(h\theta_{if}\right) \right) \right).$$
$$= \frac{2V_{\text{DC}}}{\pi h} \sum_{i=1}^{K} \left(e^{-jh\theta_{ir}} - e^{-jh\theta_{if}} \right)$$
(A7)

The envelope of the maximum magnitude of $|V_{\text{HB-SHE}/C-h}|$ is given by

$$\left| V_{\text{HB}-\text{SHE}/C-h} \right| \leq \frac{2V_{\text{DC}}}{\pi h} \sum_{i=1}^{K} \left(\left| e^{-jh\theta_{ir}} \right| + \left| e^{-jh\theta_{if}} \right| \right)$$
$$\leq \frac{4V_{\text{DC}}K}{\pi h}. \tag{A8}$$

The equality holds and only holds when $h\theta_{1r} = h\theta_{2r} + 2k\pi = \ldots = h\theta_{Kr} + 2k\pi = h\theta_{1f} + k\pi = h\theta_{2f} + k\pi$ = $\ldots = h\theta_{Kf} + k\pi$.

The harmonic order boundary between constraints (A5) and (A8) can be derived by equating (A5) and (A8). It is found as h = K. The maximum voltage is, therefore, given by,

$$\left|V_{\mathrm{HB-SHE}/C}\right|_{\mathrm{max}} = \begin{cases} 4V_{\mathrm{DC}}/\pi, & h \le K\\ 4KV_{\mathrm{DC}}/h\pi, & h \ge K \end{cases}.$$
 (A9)

B. CHB With SHE/SHC

(A3)

As shown in Fig. 5, the dc-bus voltage is E and the number of the cells is N, the CHB's maximum dc-output voltage is NE. Similar to (A4), the magnitude of $c_{CHB-SHE/C-h}$ is

$$c_{\text{CHB-SHE}/C-h} = 2/T_g \times \int_0^{T_g} v_{\text{CHB-SHE}/C} (t) \cos(h\omega_g t + \theta_h) dt$$
(A10)

The maximum value is limited by dc voltage limits

$$|c_{\text{CHB-SHE}/C-h}| \le 4NE/\pi.$$
 (A11)

If the total number of switching transitions in half fundamental period is K, similar to (A7), the complex voltage is

$$V_{\text{CHB-SHE}/C-h} = \frac{2E}{\pi h} \sum_{i=1}^{K} \left(e^{-jh\theta_{ir}} - e^{-jh\theta_{if}} \right).$$
(A12)

Similar to (A8), the envelope of the output voltage of CHB is

$$\left|V_{\rm CHB-SHE/C}\right| \le 4KE/(h\pi). \tag{A13}$$

The voltage is limited by both (A12) and (A13), the maximum voltage is, therefore, given by

$$\left| V_{\text{CHB-SHE}/C} \right|_{\text{max}} = \min\left(\frac{4KE}{h\pi}, \frac{4NE}{\pi}\right) = \begin{cases} 4NE/\pi, & h < K/N\\ 4KE/(h\pi), & h \ge K/N \end{cases}$$
(A14)

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